

BUCK/BOOST/BUCK-BOOST LED DRIVER

DESCRIPTION

The SD42560 is PWM control LED driver in Buck/Boost/Buck-Boost modes with thermal shutdown circuit, current limit circuit and PWM dimming circuit. And good line regulation and load regulation are available with wide voltage input.

The SD42560 adopts current mode control which provides fast transient response, excellent constant current characteristic and simple loop stabilization design. It has high efficiency: 96% for buck mode, 83% for buck-boost mode and 95% for boost mode,

FEATURES

- * 5~36V input voltage range
- * Maximum 1A output current (Buck mode)
- * 0.3 Ω built-in power MOSFET
- * PWM dimming function
- * 280kHz fixed frequency
- * Excellent constant current accuracy ±3%
- * High efficiency up to 96%.
- * Thermal shutdown
- * Cycle-by-cycle over current protection
- * Over voltage protection (boost/buck-boost mode)

ORDERING INFORMATION



APPLICATIONS

- * MR16 LED spotlight
- * LED illuminance
- * LCD backlight illumination
- * Automotive interior lighting
- * Portable Multi Media Player
- * Portable GPS device

Part No.	Package	Marking	Material	Packing
SD42560	SOP-8-225-1.27	SD42560	Pb free	Tube
SD42560TR	SOP-8-225-1.27	SD42560	Pb free	Tape & Reel
SD42560E	ESOP-8-225-1.27	SD42560E	Pb free	Tube
SD42560ETR	ESOP-8-225-1.27	SD42560E	Pb free	Tape & Reel



BLOCK DIAGRAM



ABSOLUTE MAXIMUM RATING

Characteristics	Symbol	Ratings	Unit
Supply Voltage	V _{IN}	40	V
Switch Voltage	V _{SW}	-1~V _{IN} +1	V
PWM Voltage	V _{PWM}	-0.3~6	V
Comp Voltage	V _{COMP}	-0.3~6	V
SENSE- Voltage	V _{SENSE-}	-0.3~V _{IN}	V
SENSE+ Voltage	V_{SENSE^+}	-0.3~V _{IN}	V
Junction Temperature	Ti	150	°C
Lead Temperature	TL	260	°C
Storage Temperature	T _{STG}	-65~150	°C
Input Voltage	V _{IN}	5~36	V
Operating Temperature	T _{OPR}	-40~125	°C

Characteristics	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Supply Voltage	V _{IN}		5		36	V
Operating Current	l _{IN}	V _{CC} =5/36V,V _{PWM} =5V		1.5	2.0	mA
Transistor Leakage Current	ار	V _{SW} =36V		0	5	μA
Switch Transistor on Resistance	R _{ON}	1LED, I _{OUT} =0.5A		0.3	0.5	Ω
Current Limit	I _{LIM}	SENSE+ is connected to SENSE-	1.7	2.0	2.3	A
Maximum Duty Cycle	Dmax	3LED, V _{IN} =9V		93	95	%
Oscillator Frequency	f _{osc}		250	280	320	kHz
PWM Dimming On Voltage	VPWMON	V _{PWM} is from 0V to 5V, On voltage	2.2	2.4	2.6	V
PWM Dimming Off Voltage	V _{PWMOFF}	V _{PWM} is from 5V to 0V, Off voltage	1.9	2.0	2.1	V
PWM Pull-Down Current	I _{PWM1}	V _{PWM} =5V	14	15	18	μA
PWM dimming frequency	F _{PWM}		100		2000	HZ
Comp Clamp Voltage	V _{COMP}	Open without load	1.6	1.9	2.2	V
Output over voltage protection	OVP	BOOST, without load	38	40	42	V
VDD Voltage	V _{DD}	No load	5.0	5.2	5.4	V
Sampling Voltage Threshold	V _{IN} -V _{SENSE}	Drop voltage between V_{IN} and SENSE		100		mV
Thermal Shutdown Threshold	TSD			160		°C
Thermal Shutdown Hysteresis	TSD-hys			30		°C

ELECTRICAL CHARACTERISTICS (Unless otherwise specified, V_{IN}=12V, I_{OUT}=700mA, T_{amb}=25°C)

PIN CONFIGURATION



Pin No.	Pin Name	I/O	Description
1	SW	Ι	Power output
2	GND	0	Ground.
3	SENSE-	Ι	Current sense- pin.
4	SENSE+	0	Current sense+ pin.
5	COMP	I/O	Compensation pin, connects to external complement
6	PWM	Ι	PWM dimming pin, connects PWM signal to change the output current.
7	V _{DD}	Ι	5V reference output
8	V _{IN}	I	Input voltage

PIN DESCRIPTION

FUNCTION DESCRIPTION

The SD42560 is the current mode LED driver used in three modes for different applications: Buck, Boost and Buck-Boost for different LEDs.

Buck mode principle: at the beginning of a cycle, internal transistor is triggered on by the signal output from OSC. SW outputs low, and the circuit loop is composed of V_{IN} , R_S , LED, inductor, internal transistor and GND. Current flowing through the inductor increases and is sensed by SENSE- and SENSE+, the error amplifier signal between the sensed value and the threshold value is compared with transistor peak current. Transistor is off when its peak current is up to the error amplifier signal. And energy of inductor is released through the circuit loop comprised by R_S , LED, inductor, Schottky diode, with current decreases. When the next clock pulse comes, transistor is on for the next period.

Boost mode principle: at the beginning of a cycle, internal transistor is triggered on by the signal output from OSC. SW outputs low and the circuit loop is composed of V_{IN} , inductor, internal transistor and GND, and current flowing through the inductor increases. Current in LED is sensed by SENSE- and SENSE+, the error amplifier signal between the sensed value and the threshold value is compared with transistor peak current. Transistor is off when its peak current is up to the error amplifier signal. And LED is powered by the circuit comprised by V_{IN} , inductor, Schottky diode, R_S , LED and GND with power supply of power drop on V_{IN+} inductor. When the next clock pulse comes, transistor is on for the next period.

Buck—Boost mode principle: at the beginning of a cycle, internal transistor is triggered on by the signal output from OSC. SW outputs low and the circuit loop is composed of V_{IN} , inductor, internal transistor and GND, and current flowing through the inductor increases. Current in LED is sensed by SENSE- and SENSE+, the error amplifier signal between the sensed value and the threshold value is compared with transistor peak current. Transistor is off when its peak current is up to the error amplifier signal. And LED is powered by the circuit comprised by inductor, Schottky diode, R_S , and LED, current through inductor decreases. When the next clock pulse comes, transistor is on for the next period.

1. PWM dimming function

SD42560 has internal PWM dimming function. When PWM pin connects to high level, the chip is working normally; when PWM pin is open or connects to low level, COMP pin disconnects with the chip, the charge of the capacitor is in hold state, voltage level holds, and the output of current comparator is high, transistor is off without output current. When PWM signal is high, COMP pin is connected with the chip to improve the startup speed of the chip. Adjust the output current by control the duty of external PWM signal.

The minimum setup time of SD42560 PWM dimming is less than 20μ S, and the maximum PWM dimming ratio is up to 500:1. When high dimming ratio is needed, the dimming frequency is recommended 500Hz below; or else the dimming frequency can be up to 2 KHz.

2. Output current setting

The output current is determined by the sense resistor and setting voltage. The sense voltage of difference of V_{SENSE^+} and V_{SENSE^-} (Drop voltage on R_S) is 100mV, and adjust the output current by adjusting the sense resistor R_S (refer to Typical Application Circuit).

$$\mathsf{I}_{\mathsf{OUT}} = \frac{\mathsf{V}_{\mathsf{SENSE}_+} - \mathsf{V}_{\mathsf{SENSE}_-}}{\mathsf{R}_{\mathsf{S}}}$$

3. Current limiting

SD42560 has internal current limiting function, and the voltage on COMP is clamped at about 1.9V, the output current of the power MOSFET is limited at about 2.5A by current comparator.

4. Frequency jitter

SD42560 has internal frequency jitter function to improve the EMI performance of the system. The internal frequency is hopping in a very small range to reduce the single frequency radiation which simplifies the EMI design.

5. Output over voltage protection

With boost/buck-boost mode, the IC will still work if voltage on sense resistor R_S is 0 when LED is open, and the peak current will be increased to the limit. Voltage at SENSE+ will be continuously increased if there is no protection and components will be damaged. With the over voltage protection, voltage at SENSE+ is monitored by internal OVP module, and power MOSFET is off if it is higher than 40V to guarantee the safety.

COMPONENTS SELECTION

1. Input Capacitor Selection

The input capacitor provides the pulse current when the power MOSFET is on, and capacitor is charged when the power MOSFET is off, so input voltage is kept. At the switch frequency, impedance of input capacitor should be less than that of input source to avoid high-frequency switch current from input source. The input capacitor is recommended to be larger than 10μ F, which can reduce the peak current drawn from input source and the switch noise. The input capacitor should be near to the input pin in real routing.

2. Output Capacitor Selection

Ripple current of LED can be reduced through decreasing the output voltage ripple by connected a capacitor to LED in parallel, and the circuit becomes more stable, while this capacitor will not effect the operating frequency and efficiency, but the start time will be longer by reducing the rising speed of the voltage on LED. The larger the output capacitor is, the smaller the current ripple on LED is. It is recommend to adopt the capacitor of 2.2μ F or larger.

3. Inductor Selection

The inductor is used to keep the output current constant, the bigger the inductance is, the smaller the output current ripple is, but the larger size will bring the larger serial resistance.

Inductor selection for buck mode:

The effective current (RMS current rating) of inductor should be higher than the maximum output current, and the saturation current should be higher than maximum output current by 30%. The series resistance (DCR) of inductor should be lower than 0.2Ω for improving the efficiency.

The inductance is expressed as:

$$L = V_{OUT} * \frac{V_{IN} - V_{OUT}}{V_{IN} * f * \Delta I}$$

Where, L: inductance

f: oscillation frequency

ΔI: ripple current

V_{IN}: input voltage,

V_{OUT}: output voltage

The ripple current ΔI is 30% of maximum load current and the minimum inductance can be calculated according to the equation above. The recommended inductance value is: $22\mu H \sim 47\mu H$.

Inductor selection for boost-buck mode:

The inductance is expressed as:

$$L = V_{OUT} * \frac{V_{IN}}{(V_{IN} + V_{OUT}) * f * \Delta I}$$

Recommended inductance value range: 22µH~47µH.

4. Diode Selection

The forward voltage drop of Schottky diode is very low and the reverse freewheeling current time is short, so it is usually used for freewheeling current. The voltage on diode is very high when power MOSFET is on, and the reverse voltage of diode should be higher than the input or output voltage. Rated current of freewheeling diode should be higher than the peak current of inductor, select the freewheeling diode according to practical application.

5. Notes for PCB routing

Proper PCB routing is important for system reliability and reducing noise. Multi-layer PCB can be adopted for reducing noise interference. PCB line connecting SW should be short and GND should be well grounded.

The inductor should be connected to EW as close as possible and lines connecting two ends of R_s should be short to reduce parasitic resistance and to guarantee the accuracy of sense current.



TYPICAL APPLICATION CIRCUIT



5~36V input voltage, 350mA output buck LED driver (V_{IN}>V_{OUT})



6~12V input voltage, 700mA output boost LED driver (VIN<VOUT)

TYPICAL APPLICATION CIRCUIT (Continued)



6~18V input voltage, 350mA output buck-boost LED driver

Note: the circuit and parameter above are just for reference, please take practical test parameters if there is any difference.

ELECTRICAL CHARACTERISTICS CURVE(curves are for BUCK mode, unless other otherwise noted)



BUCK Output 350mA efficiency changed following the input voltage



BUCK Output 700mA efficiency changed following the input voltage



BUCK/BOOST Output 350mA efficiency changed following the input voltage



BUCK Output 350mA current change rate following the input voltage change



BUCK Output 700mA current change rate following the input voltage





ELECTRICAL CHARACTERISTICS CURVE(Continued)



BUCK Output current change curve when input voltage is close to output voltage(350mA)



BUCK PWM dimming behavior of 1 LED(700mA)



BUCK PWM dimming behavior of 6 LEDs(700mA)



BUCK Output current change curve when input voltage is close to output voltage(700mA)



BUCK PWM dimming behavior of 3 LEDs(700mA)



BUCK PWM dimming behavior of 8 LEDs(700mA)



PACKAGE OUTLINE



Note:

Please soldering together of PCB copper foil and pad which is at the bottom of ESOP package, and enlarge copper foil on PCB for heat dissipation. Better for more plated-thru holes on PCB attached to PAD.



PACKAGE OUTLINE(continued)





MOS DEVICES OPERATE NOTES:

Electrostatic charges may exist in many things. Please take following preventive measures to prevent effectively the MOS electric circuit as a result of the damage which is caused by discharge:

- The operator must put on wrist strap which should be earthed to against electrostatic.
- Equipment cases should be earthed.
- All tools used during assembly, including soldering tools and solder baths, must be earthed.
- MOS devices should be packed in antistatic/conductive containers for transportation.

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ATTACHMENT

Revision History

Date	REV	Description	Page
2010.09.16	1.0	Original	
2010.10.20	1.1	Modify the template of Datasheet	